

AHL5216T8 Data Sheet

Ultra Low Noise Amplifier

1. Product Overview

1.1 General Description

AHL5216T8 is a low noise amplifier with very low noise and high linearity at low dc power consumption over a single matching circuit frequency of 1.2 ~ 3.0 GHz with the minimum off-chip components. It has an active bias circuit for stable current over temperature and process variation. It also has power on/off control function. The amplifier is available in TDFN8 package and passes the stringent DC, RF and reliability tests..

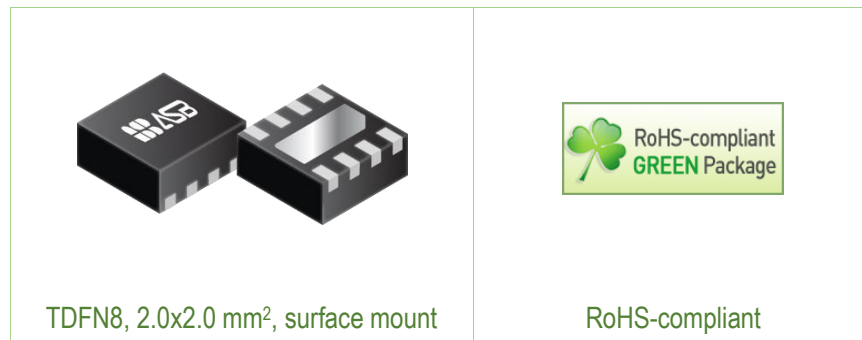
1.2 Features

- Gain decreases from 21 dB to 13 dB at 1.2~3.0 GHz (20 dB at 1.5 GHz)
- NF of 0.30~0.55 dB at 1.2~3.0 GHz (0.31 dB @ 1.5 GHz)
- 36.0 dBm OIP3 at 1.5 GHz
- 20.5 dBm P1dB at 1.5 GHz
- Integrated LNA on/off Control
- MTTF > 100 Years
- Single Supply: +5 V

1.3 Applications

- Low Noise Amplifier for L-band application
- Active Antenna
- Other Low Noise Application

1.4 Package Profile & RoHS Compliance



2. Summary on Product Performances

2.1 Typical Performance

Supply voltage = +5 V, T_A = +25 °C, Z_O = 50 Ω.

Parameter	Typical									Unit
Frequency	1200	1500	1700	1900	2000	2300	2500	2700	3000	MHz
Gain	21.3	19.8	18.8	17.8	17.3	16.0	15.1	14.4	13.4	dB
S11	-8.5	-12.0	-13.0	-13.5	-14.0	-13.5	-13.0	-12.5	-12.0	dB
S22	-9.5	-15.5	-18.0	-18.0	-17.5	-13.5	-11.0	-10.0	-8.0	dB
Noise Figure ¹⁾	0.30	0.31	0.33	0.37	0.40	0.44	0.47	0.52	0.55	dB
Output IP3 ²⁾	35.0	36.0	36.5	37.0	36.5	36.0	36.0	36.0	36.0	dBm
Output P1dB	20.5	20.5	21.0	20.5	20.0	20.0	20.0	20.0	19.0	dBm
Current	65									mA
Device Voltage	5									V

1) Noise figure measured at the connectors on the board. (i.e. not de-embedded)

2) OIP3 is measured with two tones at the output power of +4 dBm/tone separated by 1 MHz.

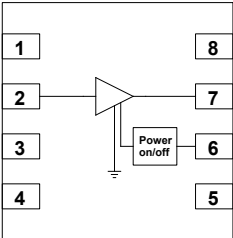
Parameter	Conditions	Min	Typ	Max	Unit
Power Shutdown Control (Vc)	On state		0		V
	Off state		1.0	Vd	V
Current	On state		65		mA
	Off state		3		mA
Switching Speed	Rise time (10% to 90%)		150		ns
	Fall time (90% to 10%)		150		ns

2.2 Product Specification

Supply voltage = +5 V, T_A = +25 °C, Z_O = 50 Ω.

Parameter	Min	Typ	Max	Unit
Frequency		2000		MHz
Noise Figure		0.40		dB
Gain		17.3		dB
S11		-14.0		dB
S22		-17.5		dB
Output IP3		36.5		dBm
Output P1dB		20.0		dBm
Current		65		mA
Device Voltage		+5		V

2.3 Pin Configuration

Pin	Description	Simplified Outline
2	RF IN	
6	Vc (on/off function)	
7	RF OUT	
1, 3, 4, 5, 8	NC or GND	
Backside paddle	GND	

2.4 Absolute Maximum Ratings

Parameters	Max. Ratings
Operation Case Temperature	-40 to +85 °C
Storage Temperature	-40 to +150 °C
Device Voltage	+6 V
Operation Junction Temperature	+150 °C
Input RF Power (CW, 50 Ω matched)	+25 dBm

2.5 Thermal Resistance

Symbol	Description	Typ	Unit
R _{th}	Thermal resistance from junction to lead	90	°C/W

2.6 ESD Classification & Moisture Sensitivity Level

ESD Classification

HBM	TBD
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CAUTION: Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

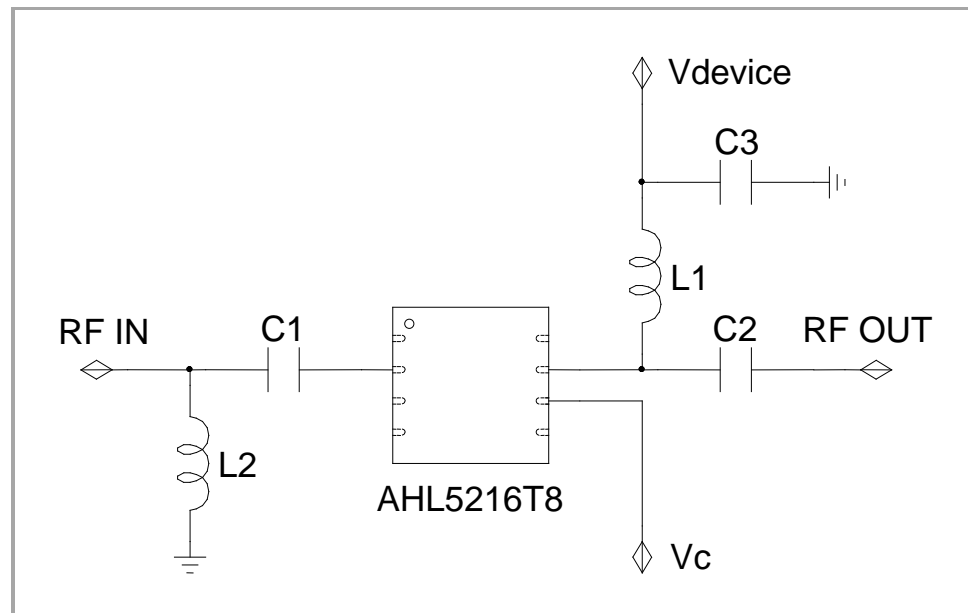
Moisture Sensitivity Level

MSL 3 at 260 °C reflow

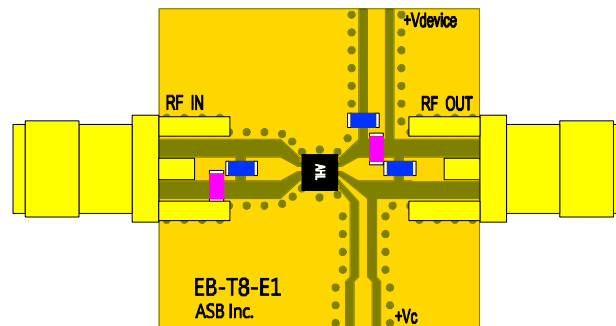
(Intentionally Blanked)

3. Application: 1200~3000 MHz ($V_{\text{DEVICE}} = +5 \text{ V}$)

3.1 Application Circuit & Evaluation Board



* Backside paddle is RF/ DC ground.



State	Vc (V)
LNA ON	0 or GND
OFF	1.0 ~ Vd

PCB Information	
Material	FR4
Thickness (mm)	0.8
Size (mm)	18x18
EB No.	EB-T8-E1

Bill of Material

Symbol	Value	Size	Description	Manufacturer
AHL5216T8	-	-	MMIC amplifier	ASB
C1	10 pF	0603	DC blocking capacitor	Murata
C2	100 pF	0603	DC blocking capacitor	Murata
C3	1 μ F	0603	Decoupling capacitor	Murata
L1	6.8 nH	0603	RF choke inductor	Murata
L2	10 nH	0603	Input matching inductor	Murata

3.2 Performance Table

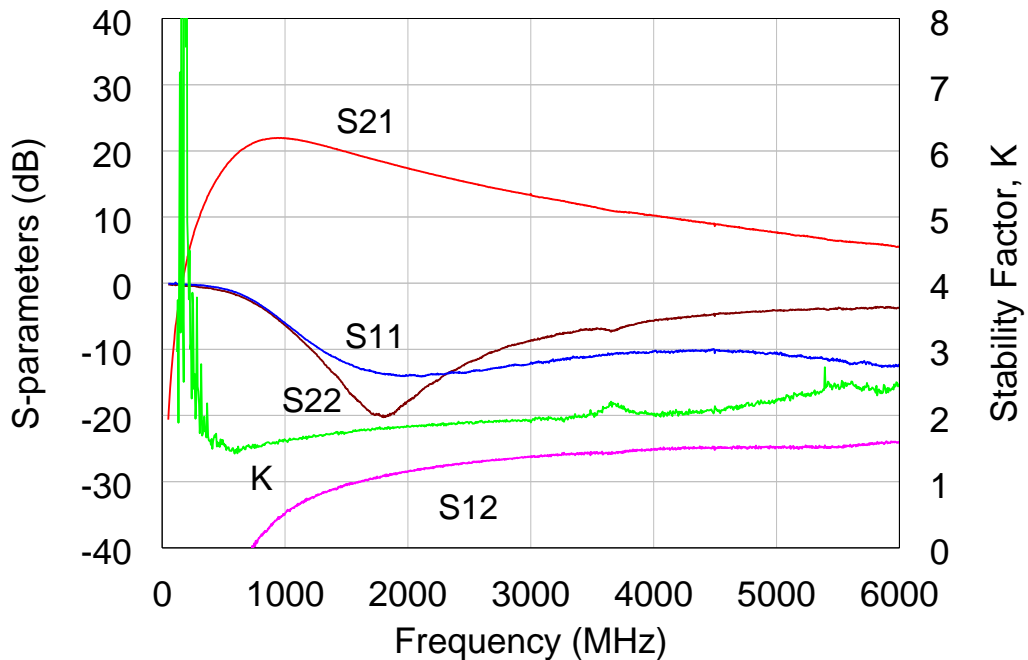
Supply voltage = +5 V, $T_A = +25\text{ }^\circ\text{C}$, $Z_0 = 50\ \Omega$.

Parameter	Typical										Unit
Frequency	1200	1500	1700	1900	2000	2300	2500	2700	3000		MHz
Gain	21.3	19.8	18.8	17.8	17.3	16.0	15.1	14.4	13.4		dB
S11	-8.5	-12.0	-13.0	-13.5	-14.0	-13.5	-13.0	-12.5	-12.0		dB
S22	-9.5	-15.5	-18.0	-18.0	-17.5	-13.5	-11.0	-10.0	-8.0		dB
Noise Figure ¹⁾	0.30	0.31	0.33	0.37	0.40	0.44	0.47	0.52	0.55		dB
Output IP3 ²⁾	35.0	36.0	36.5	37.0	36.5	36.0	36.0	36.0	36.0		dBm
Output P1dB	20.5	20.5	21.0	20.5	20.0	20.0	20.0	20.0	19.0		dBm
Current	65										mA
Device Voltage	5										V

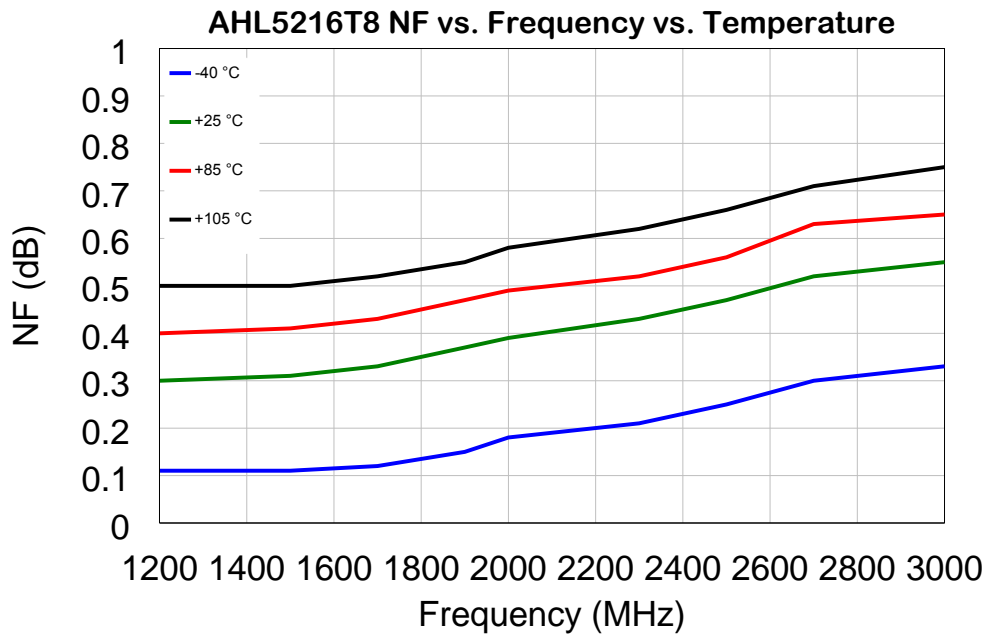
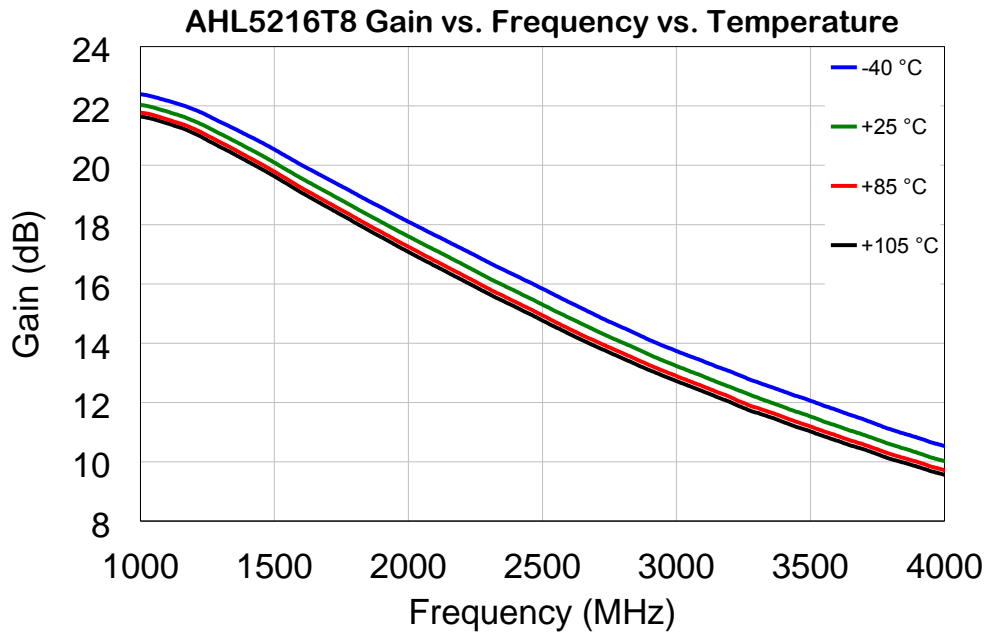
1) Noise figure measured at the connectors on the board. (i.e. not de-embedded)

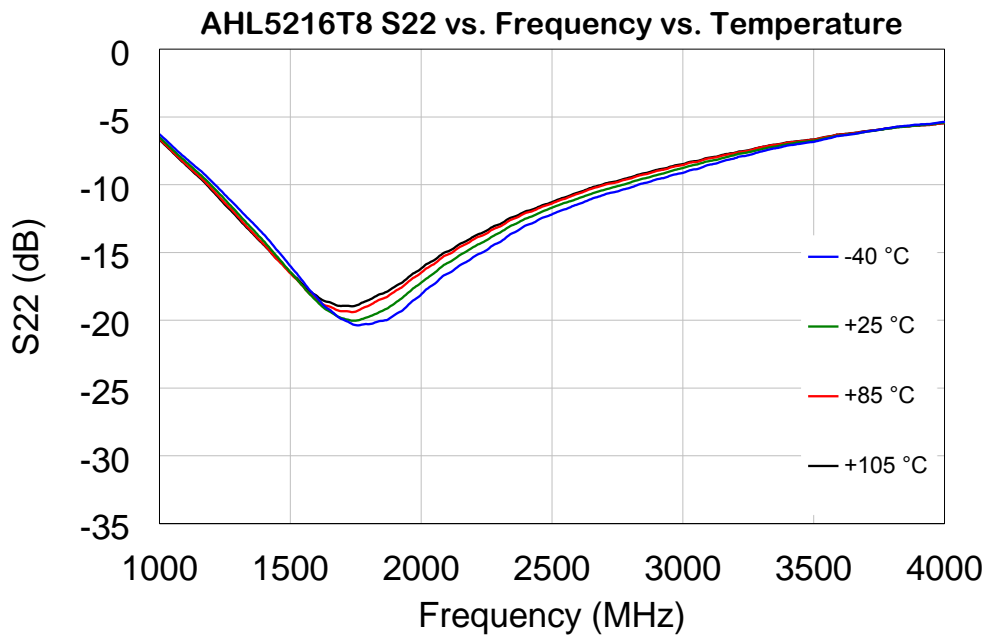
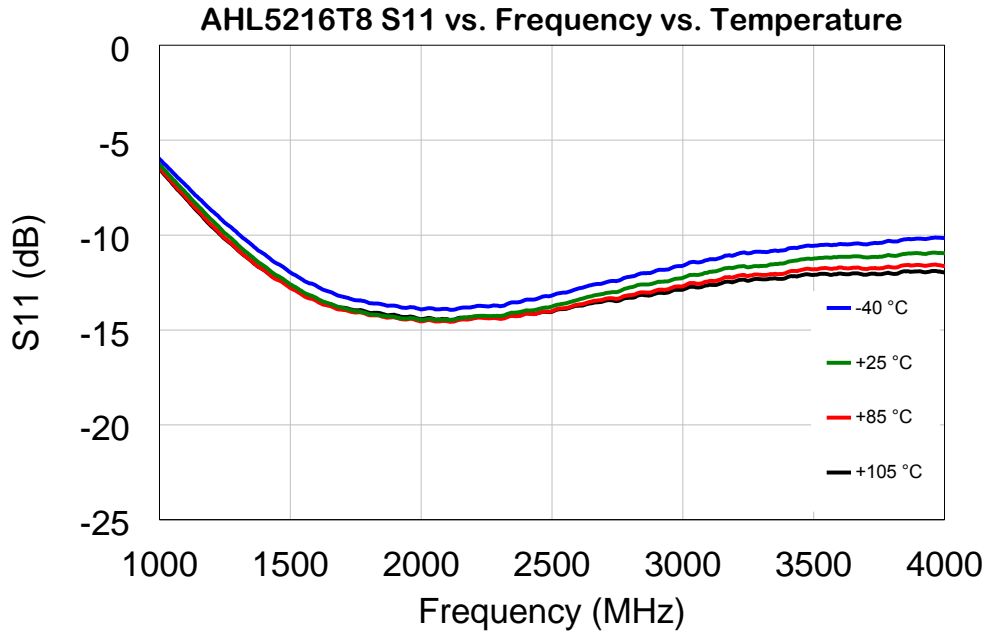
2) OIP3 is measured with two tones at the output power of +4 dBm/tone separated by 1 MHz.

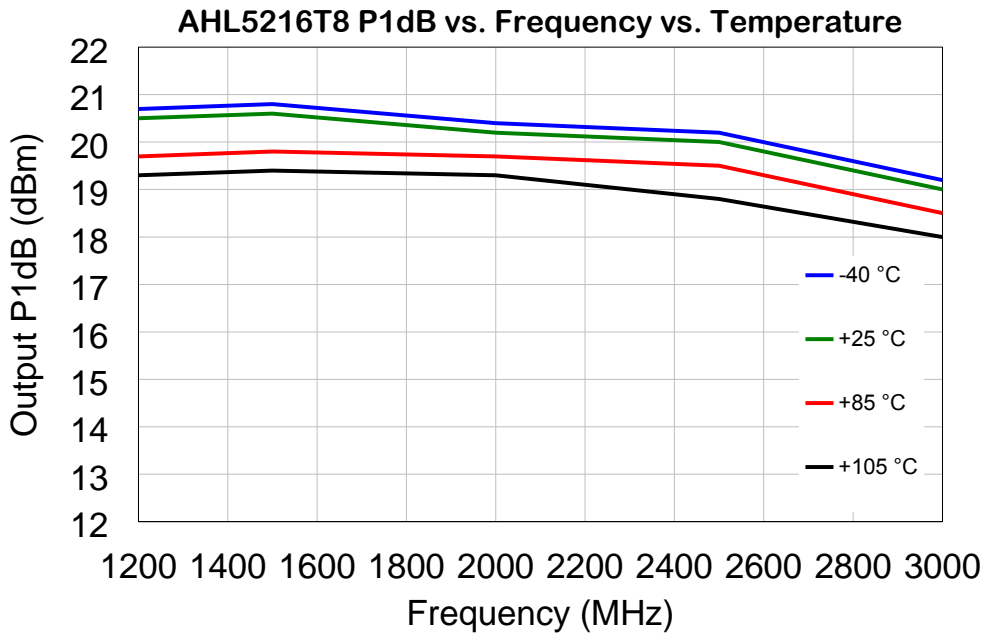
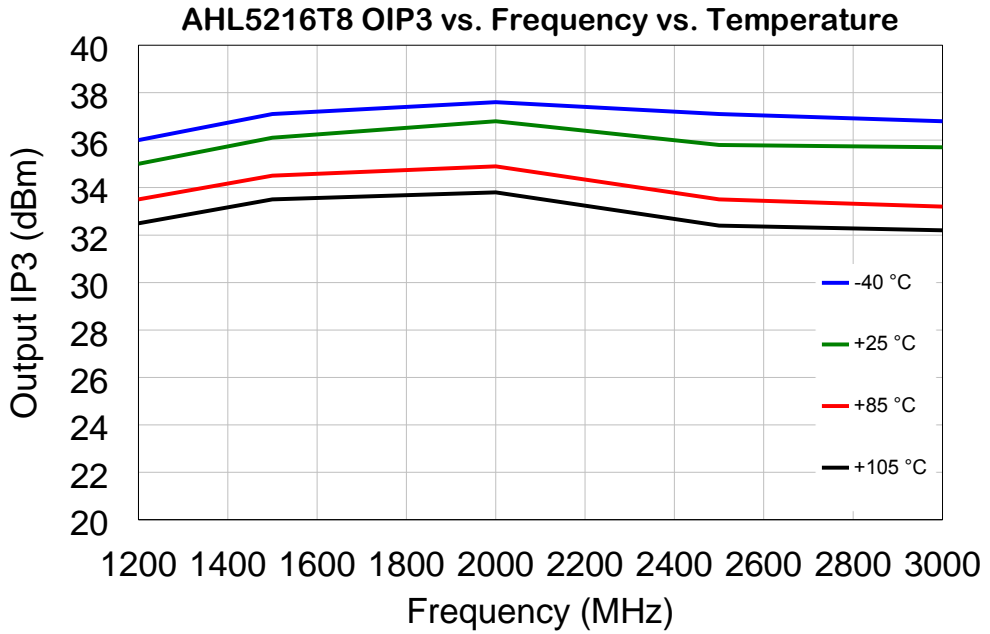
3.3 Plot of S-parameters

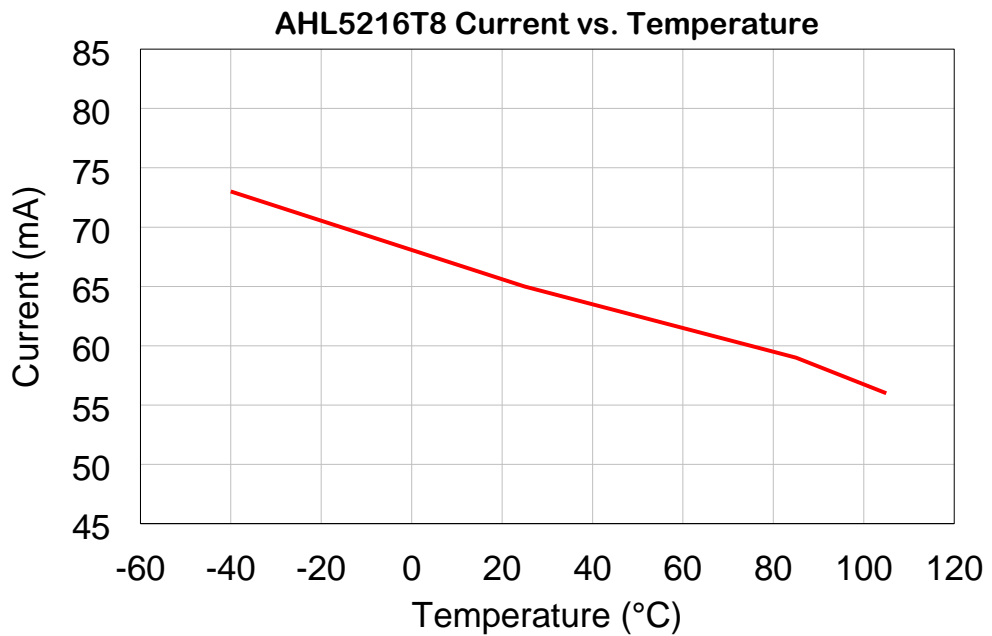
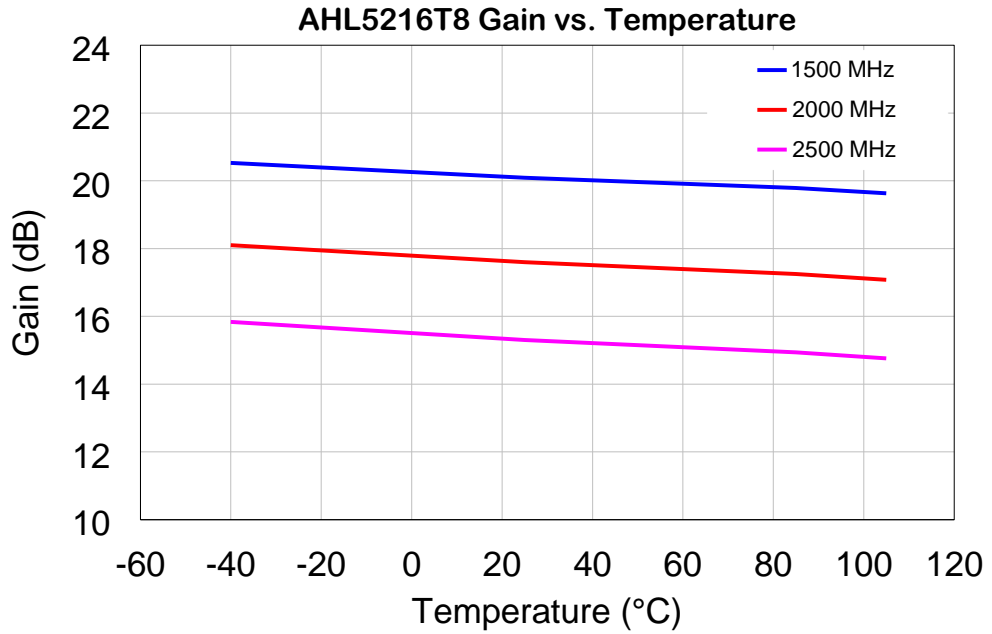


3.4 Plots of Noise Figure and Performances with Temperature



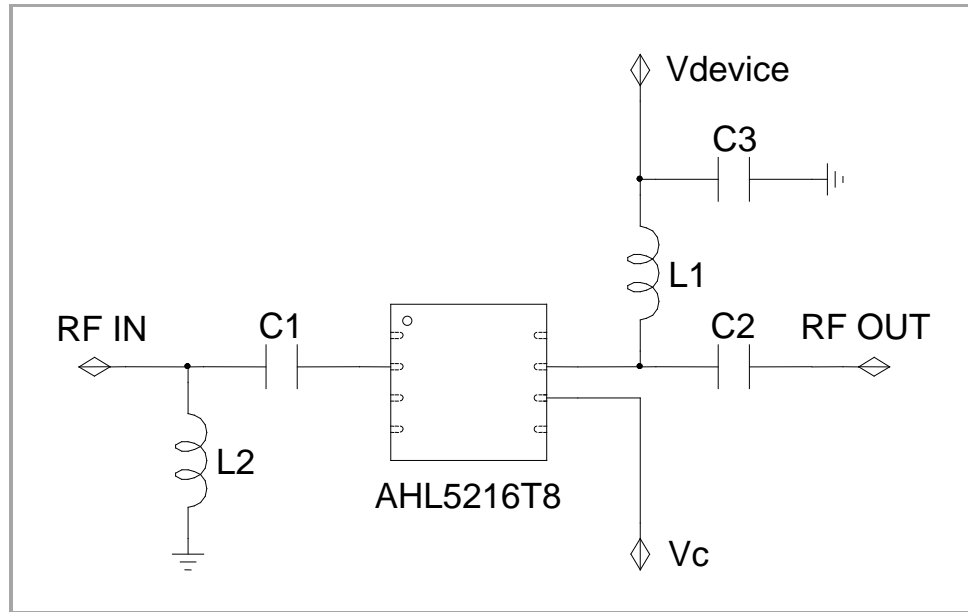




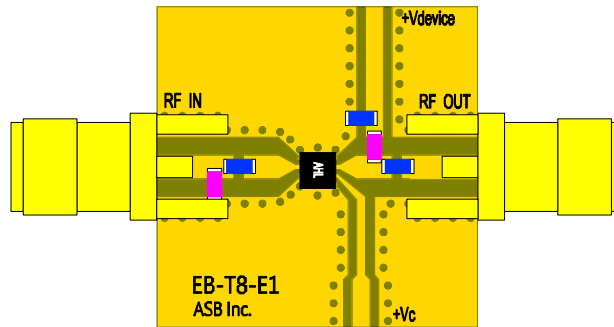


4. Application: 1100~1700 MHz ($V_{\text{DEVICE}} = +1.8, +2.5, +3.3 \text{ V}$)

4.1 Application Circuit & Evaluation Board



* Backside paddle is RF/ DC ground



State	Vc (V)
LNA ON	0 or GND
OFF	1.0 ~ Vd

PCB Information	
Material	FR4
Thickness (mm)	0.8
Size (mm)	18x18
EB No.	EB-T8-E1

Bill of Material

Symbol	Value	Size	Description	Manufacturer
AHL5216T8	-	-	MMIC Amplifier	ASB
C1	39 pF	0603	DC blocking capacitor	Murata
C2	100 pF	0603	DC blocking capacitor	Murata
C3	1 μ F	0603	Decoupling Capacitor	Murata
L1	12 nH	0603	RF Choke inductor	Murata
L2	10 nH	0603	Input matching inductor	Murata

4.2 Performance Table

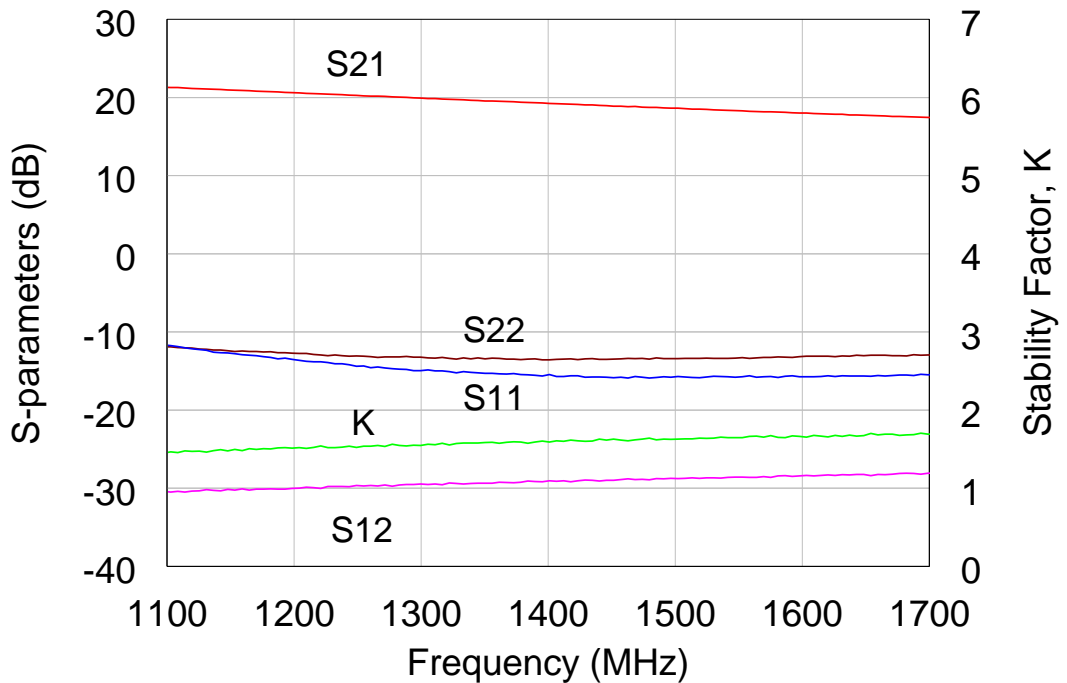
Supply voltage = +1.8, +2.5, +3.3 V, $T_A = +25\text{ }^\circ\text{C}$, $Z_0 = 50\ \Omega$.

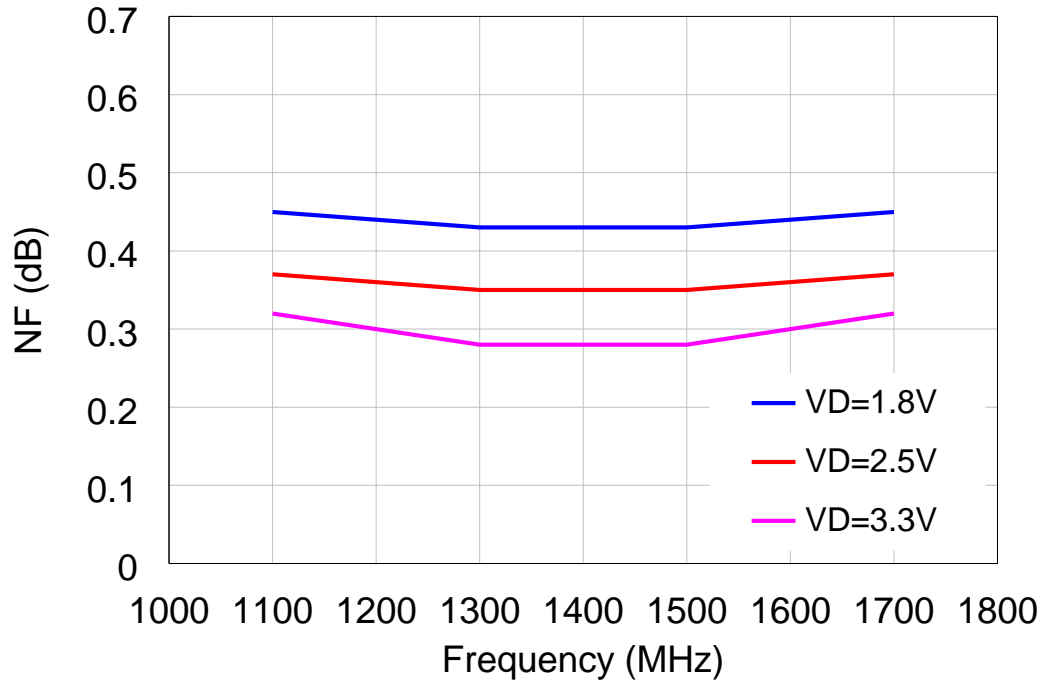
Parameter	Typical									Unit
	1100	1500	1700	1100	1500	1700	1100	1500	1700	
Frequency	1100	1500	1700	1100	1500	1700	1100	1500	1700	MHz
Gain	19.5	16.7	15.5	21.1	18.4	17.3	21.9	19.2	18.2	dB
S11	-10.0	-11.0	-11.0	-11.0	-15.0	-15.0	-11.0	-14.0	-16.0	dB
S22	-11.0	-13.0	-12.0	-11.0	-13.0	-12.0	-11.0	-13.0	-12.0	dB
Noise Figure ¹⁾	0.45	0.43	0.45	0.37	0.35	0.37	0.32	0.28	0.32	dB
Output IP3 ²⁾	18.0	18.0	18.0	24.0	24.0	24.0	29.0	29.0	29.0	dBm
Output P1dB	8.0	8.0	8.0	13.0	13.0	13.0	17.0	17.0	17.0	dBm
Current	10			20			35			mA
Device Voltage	1.8			2.5			3.3			V

1) Noise figure measured at the connectors on the board. (i.e. not de-embedded)

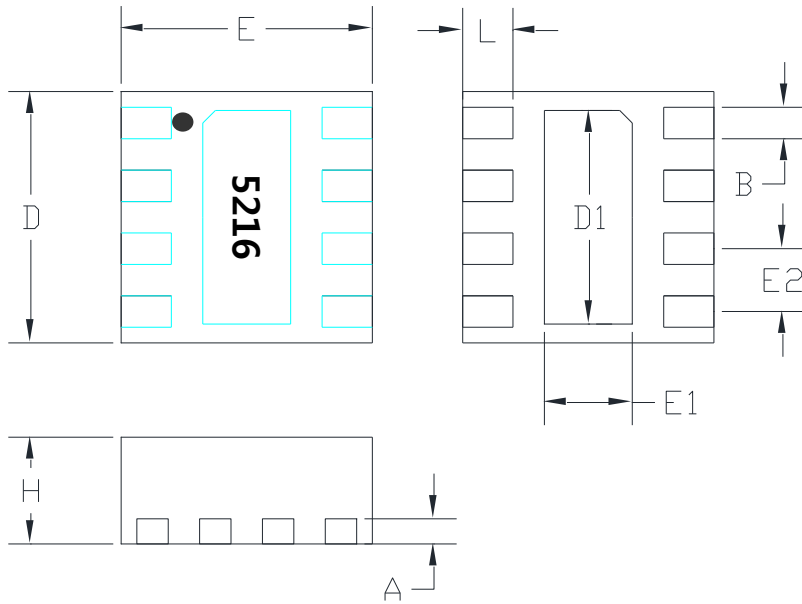
2) OIP3 is measured with two tones at the output power of +0 dBm/tone separated by 1 MHz.

4.3 Plot of S-parameters & Noise Figure



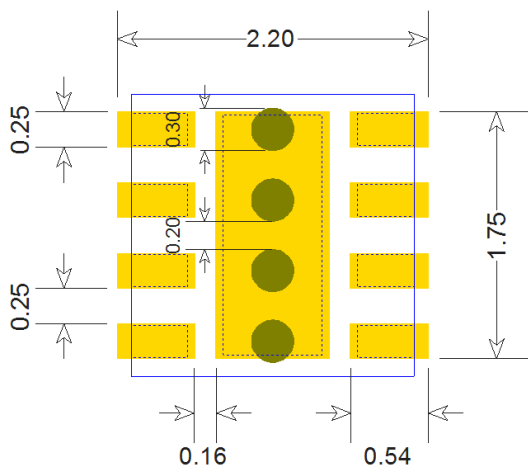


5. Package Outline (TDFN8)



Symbols	Dimensions (In mm)		
	MIN	NOM	MAX
A	-	0.20REF	-
B	0.18	0.23	0.28
D	1.95	2.00	2.03
D1	-	1.7BSC	-
E	1.95	2.00	2.03
E1	-	0.7BSC	-
E2	-	0.5BSC	-
L	0.35	0.40	0.45
H	0.80	0.85	0.90

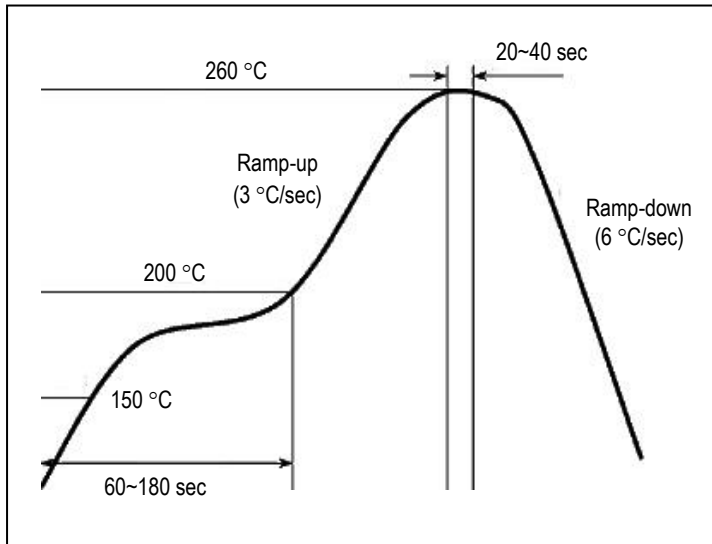
6. Surface Mount Recommendation (In mm)



NOTE

1. The number and size of ground via holes in a circuit board is critical for thermal and RF grounding considerations.
2. Recommend is that the ground via holes be placed on the bottom of the lead pin 2 and exposed pad of the device for better RF and thermal performance, as shown in the drawing at the left side.

7. Recommended Soldering Reflow Profile



(End of Datasheet)

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